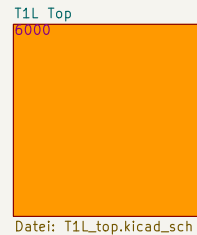


BE-IIS-HAT++2T1L Rev.A

Brechel Electronic

Industrie Interface Solutions

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Brechel Electronic
Industrial Interface Solutions
Designed by Philipp Brechel (Germany)
www.be-iis.eu
contact@be-iis.eu

Sheet: /
File: BE-IIS-HATPP-2T1L.kicad_sch

Title: Pi-HAT+: T1L

Size: A4 Date: 2026-08-21

KiCad E.D.A. 10.0.3

Rev: A

Id: 1/8

This figure displays four schematic diagrams related to the Pi-HAT+ board, showing connections to an EEprom, T1L, Pi Pinout, and I2C-Pullups.

HAT+ EEprom: Shows the connection of the EEprom (uC_EEE.kicad_sch) to the Pi-HAT+ board. The EEprom is connected to the Pi-HAT+ pins via a 000_my_Connectors:PH connector. The EEprom is connected to the Pi-HAT+ pins via a 000_my_Connectors:PH connector.

T1L: Shows the connection of the T1L (T1L_sch.kicad_sch) to the Pi-HAT+ board. The T1L is connected to the Pi-HAT+ pins via a 000_my_Connectors:PH connector. The T1L is connected to the Pi-HAT+ pins via a 000_my_Connectors:PH connector.

Pi Pinout: Shows the pinout of the Pi-HAT+ board (A6000A, A6000C, A6000D) connected to the Pi-HAT+ pins. The Pi-HAT+ pins are connected to the Pi-HAT+ pins via a 000_my_Connectors:PH connector.

I2C-Pullups: Shows the connection of the I2C-Pullups (I2C_Pull-Up_EN.kicad_sch) to the Pi-HAT+ board. The I2C-Pullups are connected to the Pi-HAT+ pins via a 000_my_Connectors:PH connector.

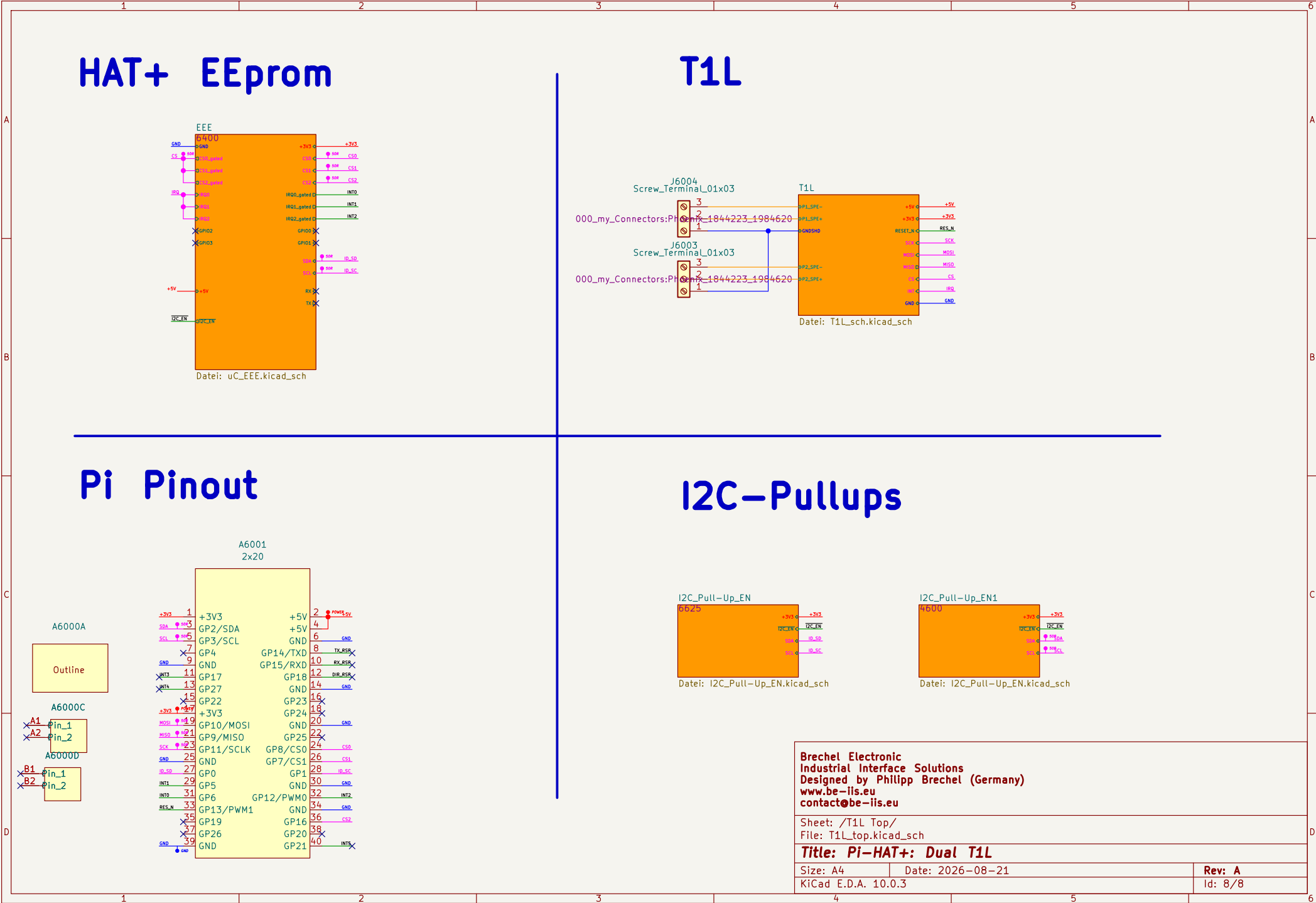
This figure displays four schematic diagrams related to the Pi-HAT+ board, showing connections to an EEprom, T1L, Pi Pinout, and I2C-Pullups.

HAT+ EEprom: Shows the connection of the EEprom (uC_EEE.kicad_sch) to the Pi-HAT+ board. The chip is connected to the +3V3 and GND pins. The CS pin is connected to the CS pin of the Pi-HAT+ board. The INT pin is connected to the INT pin of the Pi-HAT+ board. The chip is also connected to the +5V and GND pins.

T1L: Shows the connection of the T1L (T1L_sch.kicad_sch) to the Pi-HAT+ board. The chip is connected to the +3V3 and GND pins. The SPI pins (SP1_SPE-, SP1_SPE+, SP2_SPE-, SP2_SPE+) are connected to the SPI pins of the Pi-HAT+ board. The RESET pin is connected to the RESET pin of the Pi-HAT+ board. The chip is also connected to the +5V and GND pins.

Pi Pinout: Shows the pinout of the Pi-HAT+ board (A6000A, A6000C, A6000D). The pins are labeled with their functions and the corresponding pin numbers on the board. The pins are color-coded: red for power, green for ground, blue for I2C, and yellow for SPI.

I2C-Pullups: Shows the connection of the I2C-Pullups (I2C_Pull-Up_EN.kicad_sch) to the Pi-HAT+ board. The chip is connected to the +3V3 and GND pins. The I2C pins (SDA, SCL) are connected to the I2C pins of the Pi-HAT+ board. The chip is also connected to the +5V and GND pins.



This figure displays four schematic diagrams for a Pi-HAT+ board, showing the connection of various components to the Raspberry Pi 40-pin header.

HAT+ EEprom: Shows the connection of an EEprom (uE00) to the Pi header. The EEprom is connected to the header pins for +3V3, GND, CS, SDA, SCL, and INT2. The date is uC_EEE.kicad_sch.

T1L: Shows the connection of a T1L (T1L) to the Pi header. The T1L is connected to the header pins for +5V, +3V3, RESET_N, SCK, MOSI, MISO, CS, IRQ, and GND. The date is T1L_sch.kicad_sch.

Pi Pinout: Shows the pinout of the Pi header (A6000A, A6000C, A6000D) and the connection of the A6001 2x20 header. The A6001 header is connected to the Pi header pins for +3V3, +5V, GND, TXD, RXD, DIR, CS0, CS1, ID_SC, INT0, INT1, INT2, GP13/PWM1, GP19, GP26, and GND. The date is I2C_Pull-Up_EN.kicad_sch.

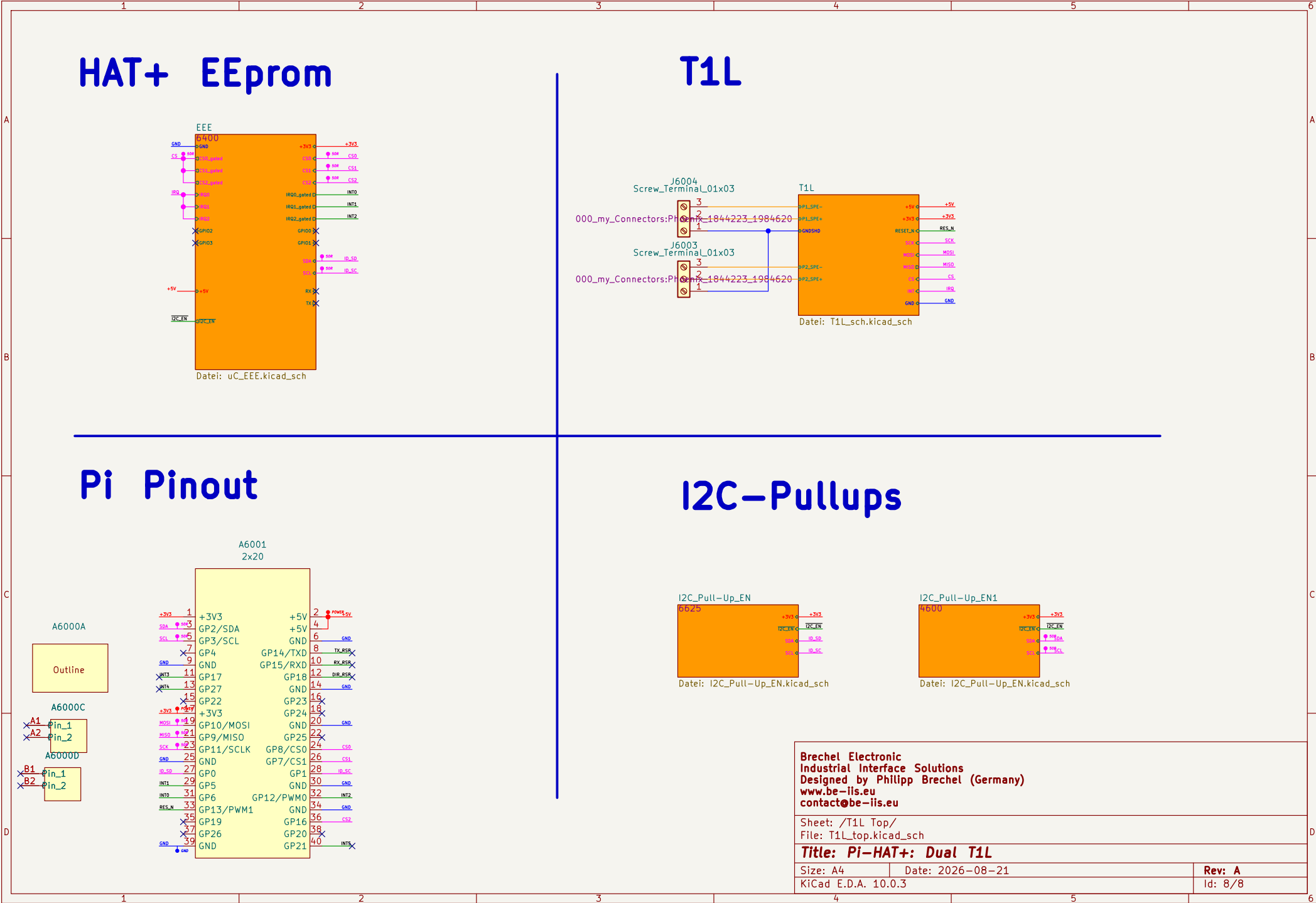
I2C-Pullups: Shows the connection of I2C pull-up resistors (I2C_Pull-Up_EN) to the Pi header. The I2C_Pull-Up_EN is connected to the header pins for +3V3, I2C_EN, ID_SC, and GND. The date is I2C_Pull-Up_EN.kicad_sch.

Legend:

- A6000A: Outline
- A6000C: Pin_1, Pin_2
- A6000D: Pin_1, Pin_2

Metadata:

- Sheet: /T1L Top/
- File: T1L_top.kicad_sch
- Title: Pi-HAT+: Dual T1L
- Size: A4
- Date: 2026-08-21
- KiCad E.D.A. 10.0.3
- Rev: A
- Id: 8/8



This figure displays four schematic diagrams for a Pi-HAT+ board, showing the connection of various components to the Raspberry Pi 40-pin header.

HAT+ EEprom: Shows the connection of an EEprom (uE00) to the Pi header. The EEprom is connected to the header pins for +3V3, GND, CS, SDA, SCL, and INT2. The date is uC_EEE.kicad_sch.

T1L: Shows the connection of a T1L (T1L) to the Pi header. The T1L is connected to the header pins for +5V, +3V3, RESET_N, SCK, MOSI, MISO, CS, IRQ, and GND. The date is T1L_sch.kicad_sch.

Pi Pinout: Shows the pinout of the Pi header (A6000A, A6000C, A6000D) and the connection of the A6001 2x20 header. The A6001 header is connected to the Pi header pins for +3V3, +5V, GND, TXD, RXD, DIR, CS0, CS1, ID_SC, INT0, INT1, INT2, GP13/PWM1, GP19, GP26, and GND. The date is I2C_Pull-Up_EN.kicad_sch.

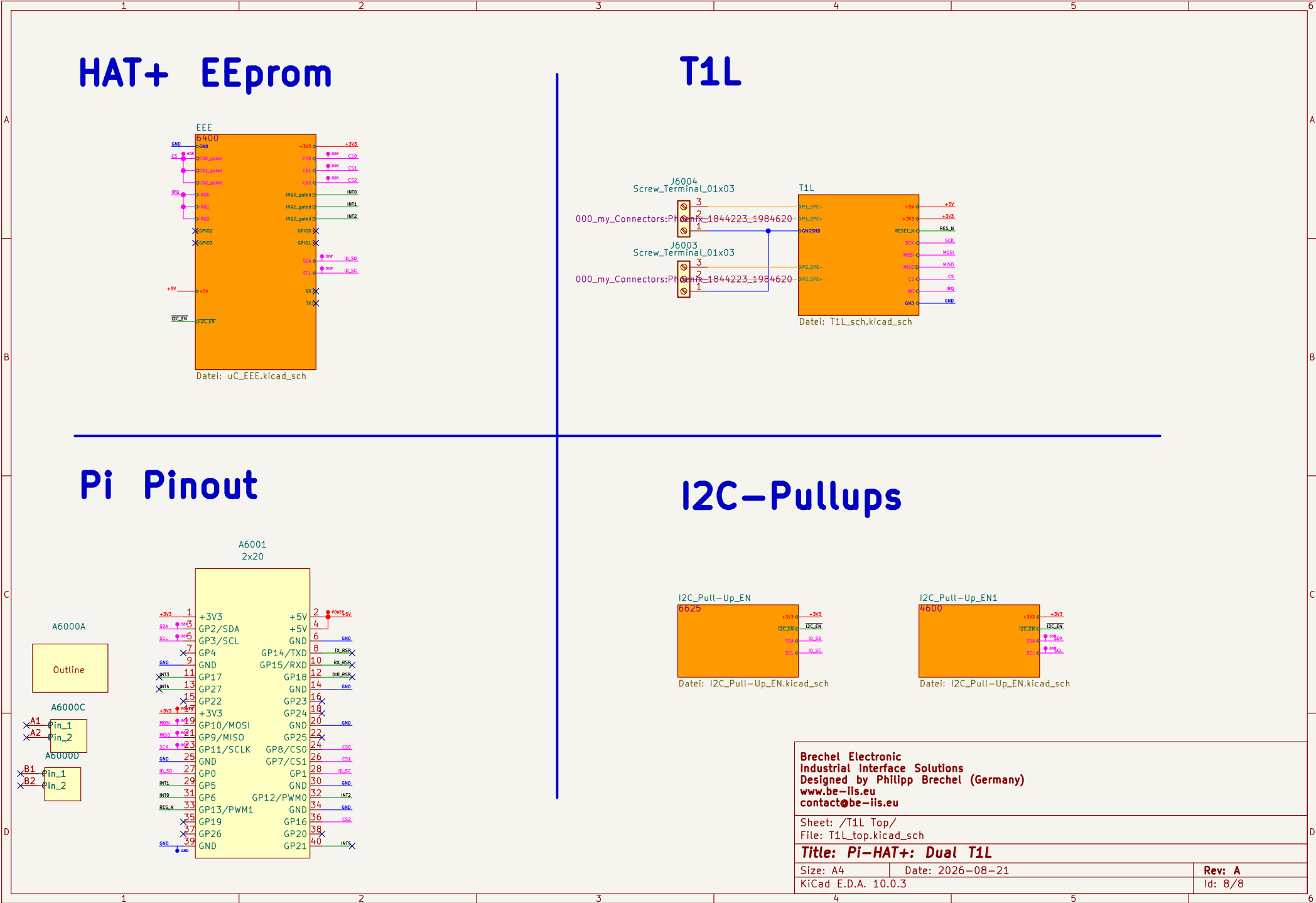
I2C-Pullups: Shows the connection of I2C pull-up resistors (I2C_Pull-Up_EN) to the Pi header. The I2C_Pull-Up_EN is connected to the header pins for +3V3, I2C_EN, ID_SC, and GND. The date is I2C_Pull-Up_EN.kicad_sch.

Legend:

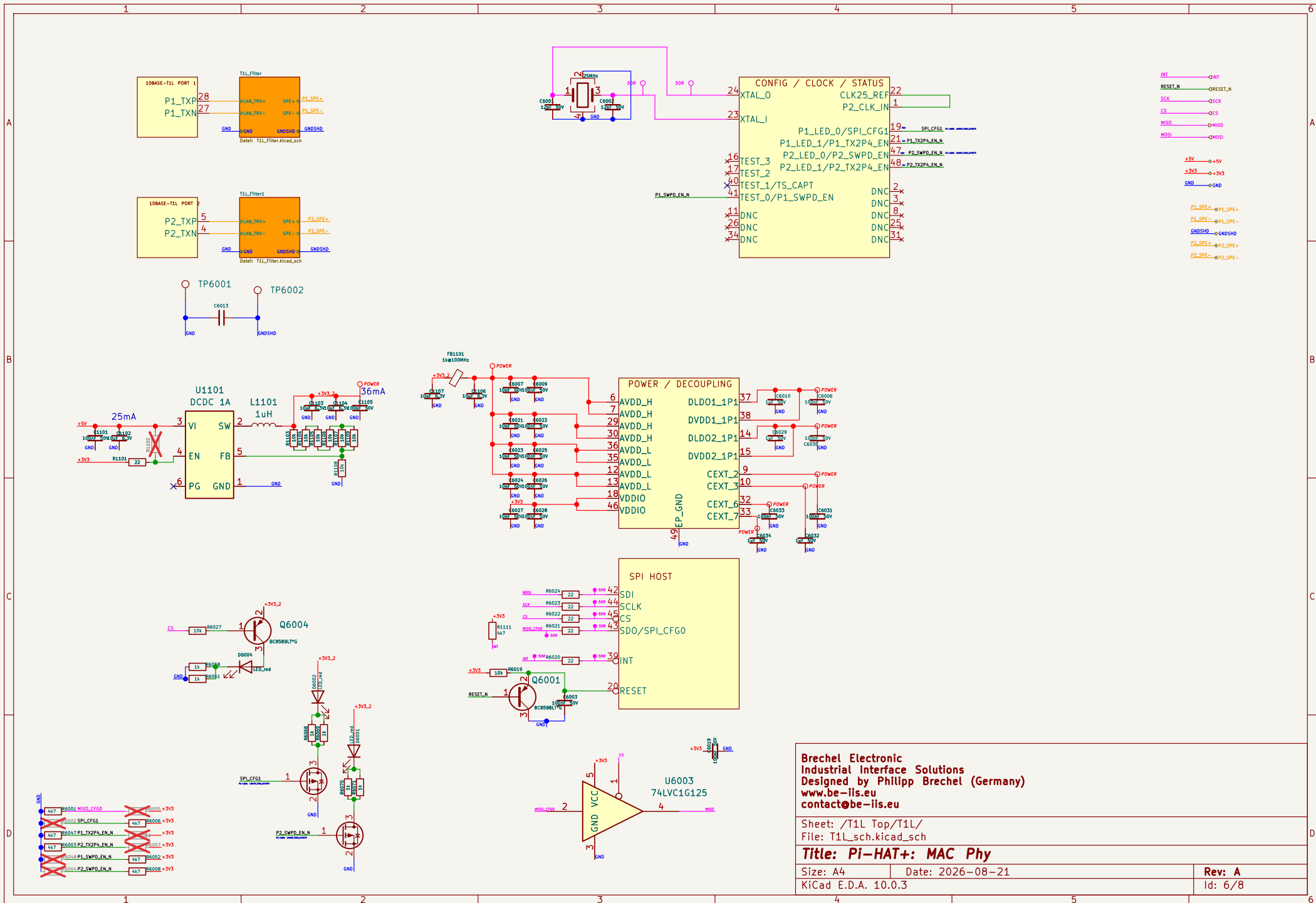
- A6000A: Outline
- A6000C: Pin_1, Pin_2
- A6000D: Pin_1, Pin_2

Metadata:

- Sheet: /T1L Top/
- File: T1L_top.kicad_sch
- Title: Pi-HAT+: Dual T1L
- Size: A4
- Date: 2026-08-21
- KiCad E.D.A. 10.0.3
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Sheet: /T1L Top/ File: T1L_top.kicad_sch	
Title: Pi-HAT+: Dual T1L	
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contact@be-iis.eu

Sheet: /T1L Top/T1L/
File: T1L_sch.kicad_sch

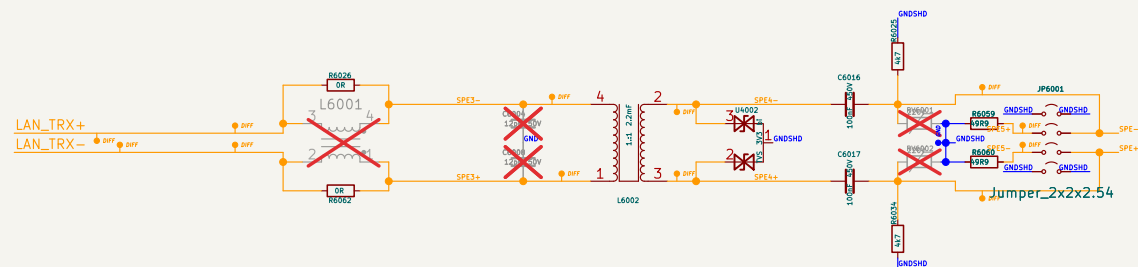
Title: Pi-HAT+: MAC Phy

Size: A4 Date: 2026-08-21

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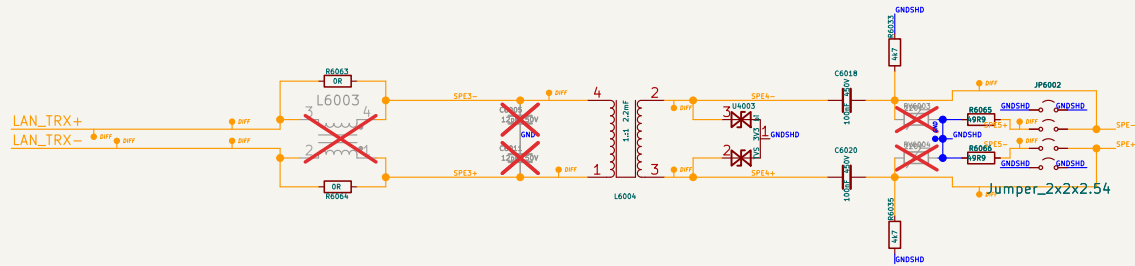
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SPE+ → SPE+
SPE- → SPE-
GNDSHD → GNDSHD
LAN_TRX+ → LAN_TRX+
LAN_TRX- → LAN_TRX-
GND → GND



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Designed by Philipp Brechel (Germany)
www.be-iis.eu
contact@be-iis.eu

Sheet: /T1L Top/T1L/T1L_Filter1/
File: T1L_Filter.kicad_sch

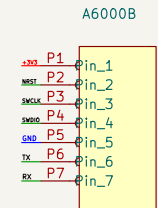
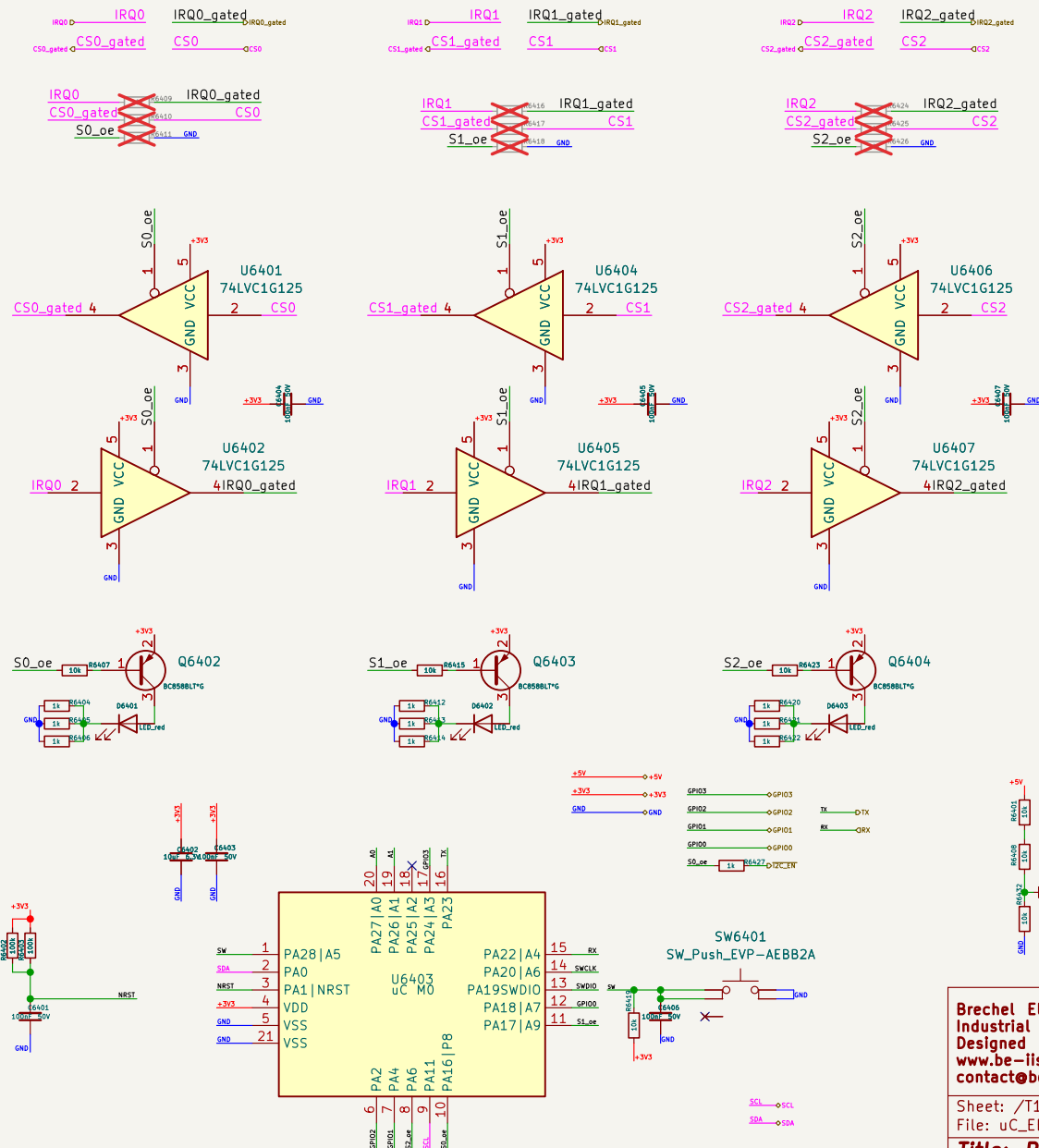
Title: Pi-HAT++: T1S Filter

Size: A4 Date: 2026-08-21

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Rev: A

Id: 3/8



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Sheet: /T1L Top/EEE/ File: uC_EEE.kicad_sch		
Title: Pi-HAT+: EEPROM-Emulation and SPI configuration		
Size: A4	Date: 2026-08-21	Rev: A
KiCad E.D.A. 10.0.3		Id: 15/8

